

FIG. 1

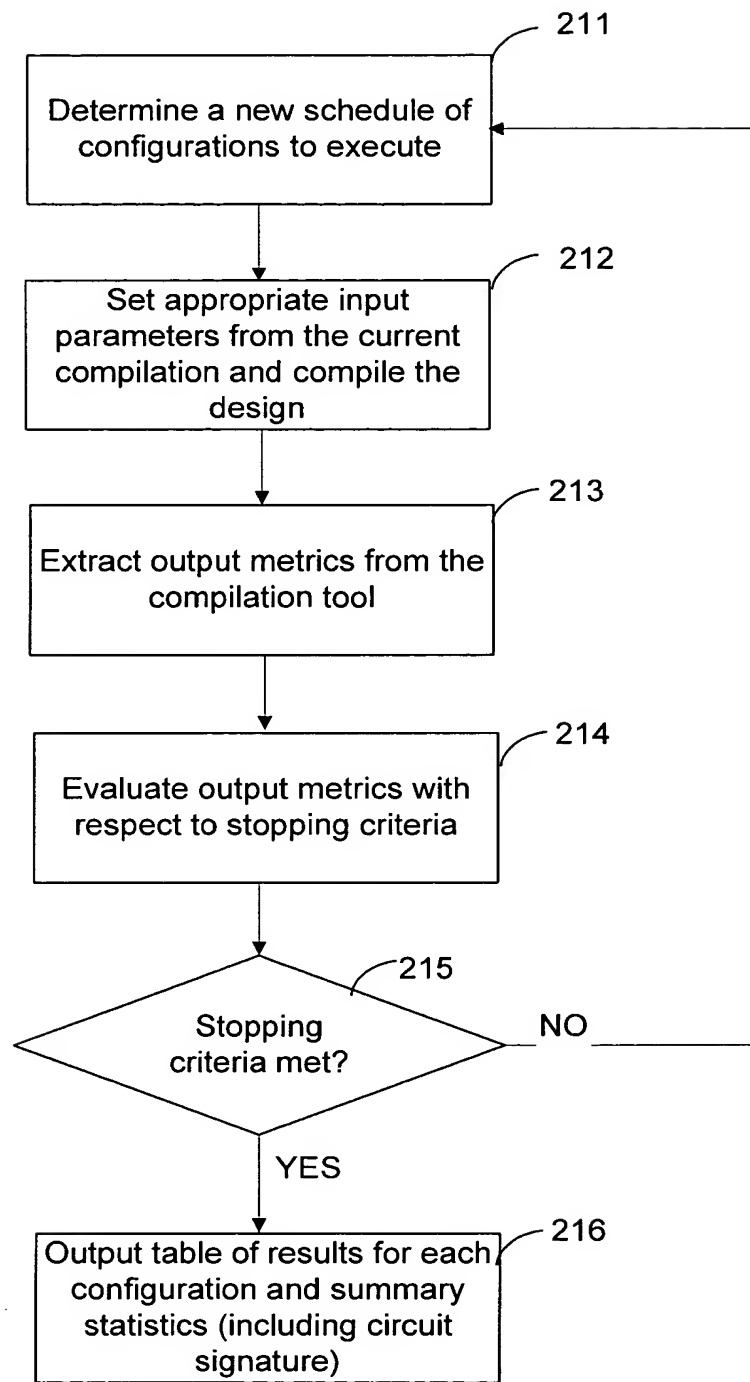


FIG. 2

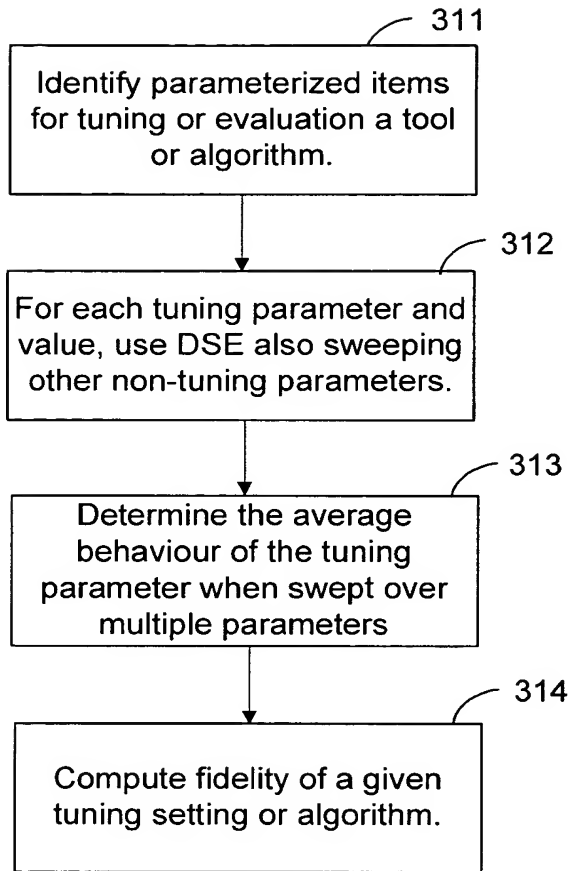


FIG. 3a

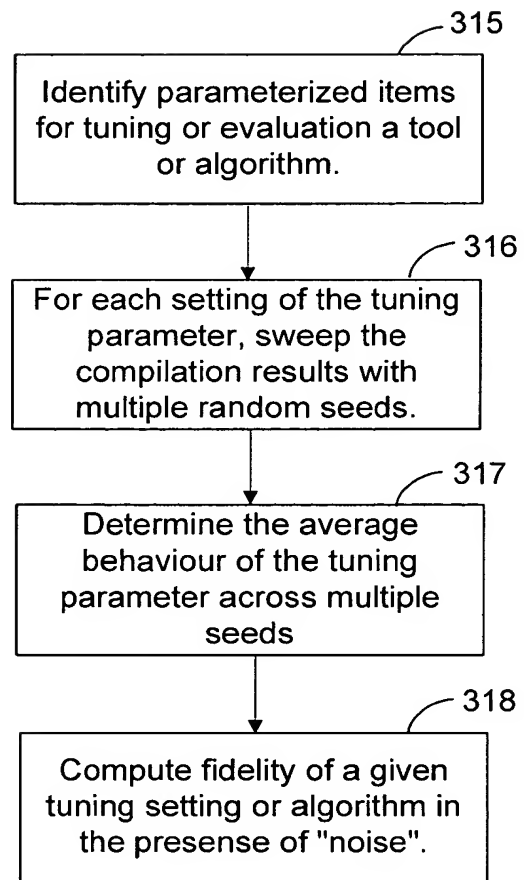


FIG. 3b

400

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Project and Seed Settings:

Project:

Seeds to Sweep:

402

Exploration Type:

☒ Type 1: Seed sweeping, register packing, and making the fitter try harder

☐ Type 2: Type 1 plus LE duplication, LUT resynthesis and WYSIWYG remapping

☐ Type 3: Type 2 plus register retiming

☐ Custom Exploration

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Exploration Options:

Stop exploration after this much time has elapsed Hours

Stop exploration after this gain in period is achieved %

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Compilation Output Data Displayed Here

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Best Results:

Period: 10 ns
Slack: -288 ns
Failing Paths: 2
Compile Time: 50s
Logic cell count: 5

406

☒ Archive each compile

☒ Show compilation results

FIG. 4

